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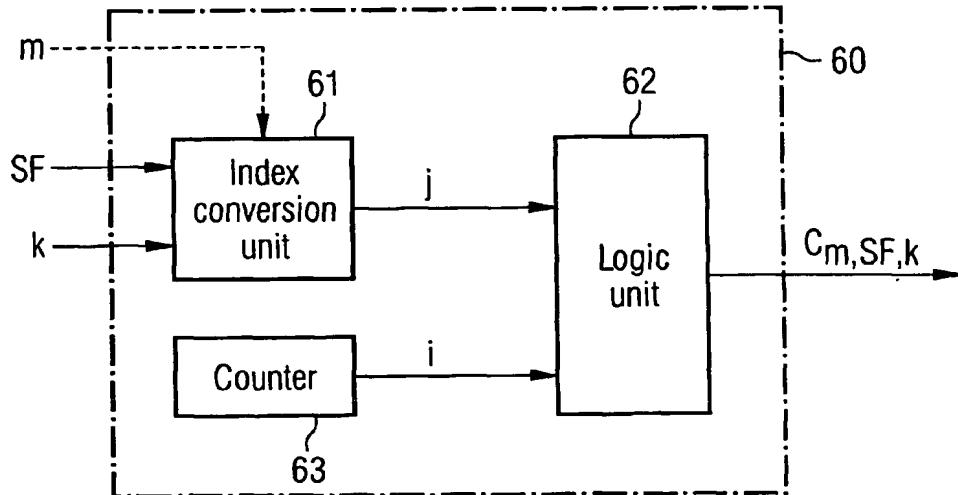
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(54) Title: GENERATION OF ORTHOGONAL CODES



(57) Abstract: This invention relates to code generators for generating an orthogonal code having a spreading factor SF and an index k, wherein the spreading factor SF is selectable from values in a range $1 < SF \leq SF_{max}$ with SF_{max} denoting a maximum spreading factor, said code generator including an index conversion unit for converting said index k into a modified index j associated with a corresponding code having the maximum spreading factor, and further including a logic unit for performing logic operations on bits of said modified index j and bits of a counter value i, thereby generating a code bit of said orthogonal code. The invention further relates to parallel code generators for concurrently generating $P > 1$ orthogonal codes having respective spreading factors and indices. Moreover, the invention relates to corresponding methods for generating an orthogonal code as well as to computer program products comprising software code portions for performing the steps of said methods.